

FIG. 1 The DigiAlbum Top-Level Structure

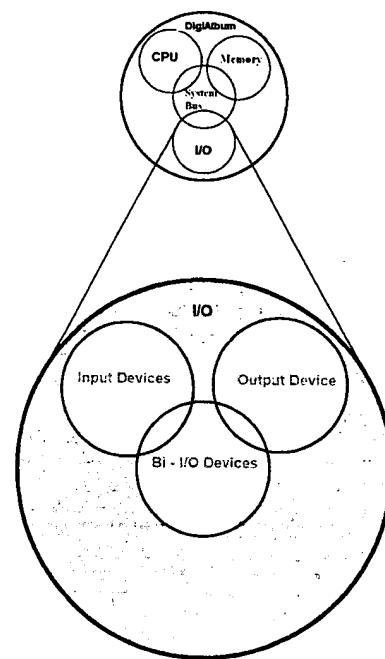


FIG. 2      The I/O

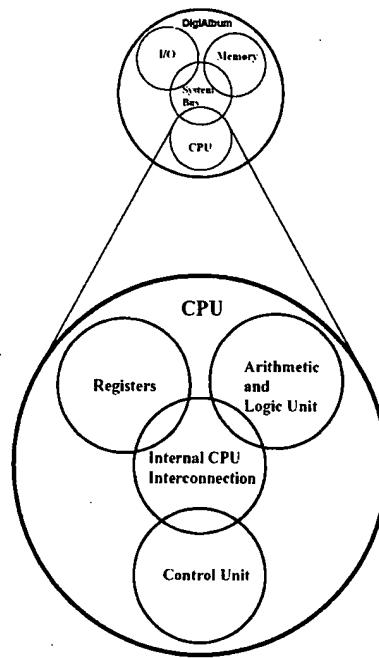


FIG. 3A The Central Processing Unit (CPU)

Central processing unit (CPU)

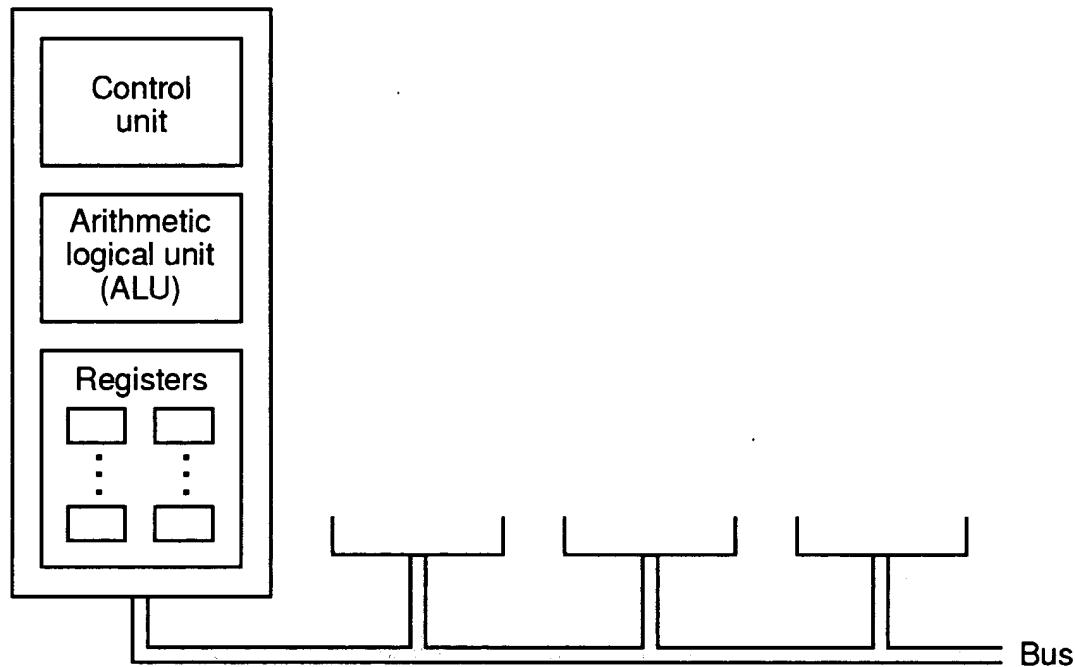


FIG. 3B: Top-Level Structure of the CPU on a System Bus

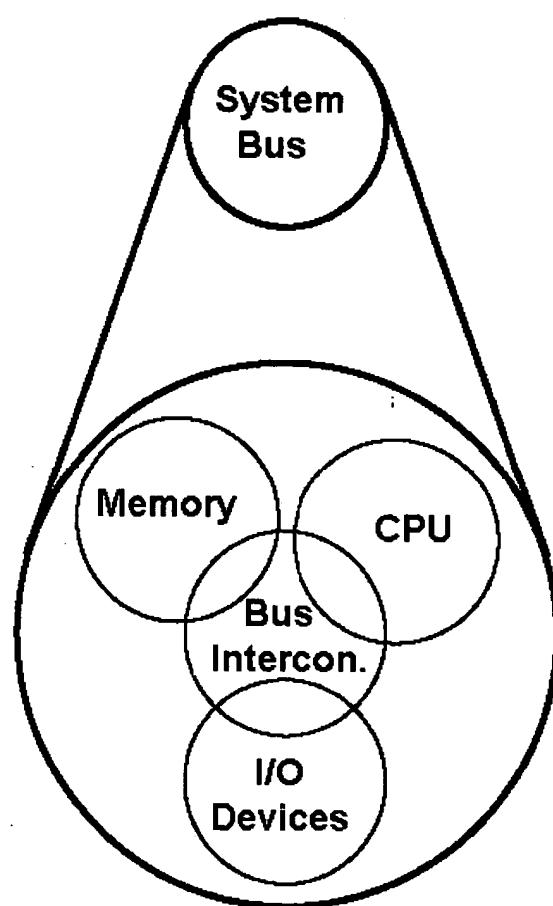


FIG. 4: The System Bus Top-Level Structure

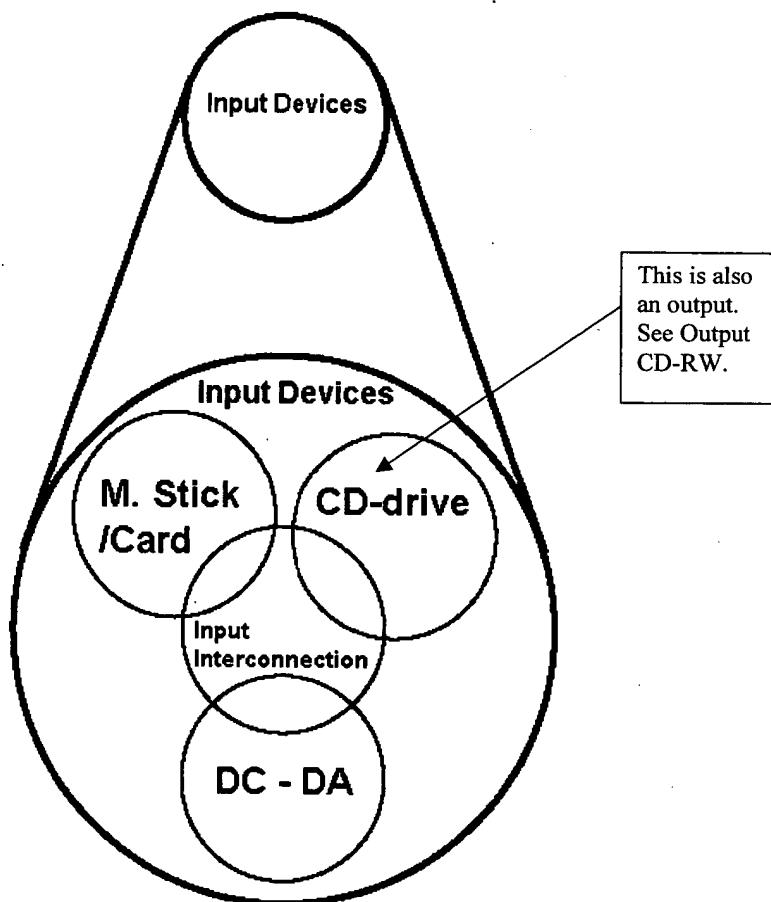


FIG. 5      The Input Devices

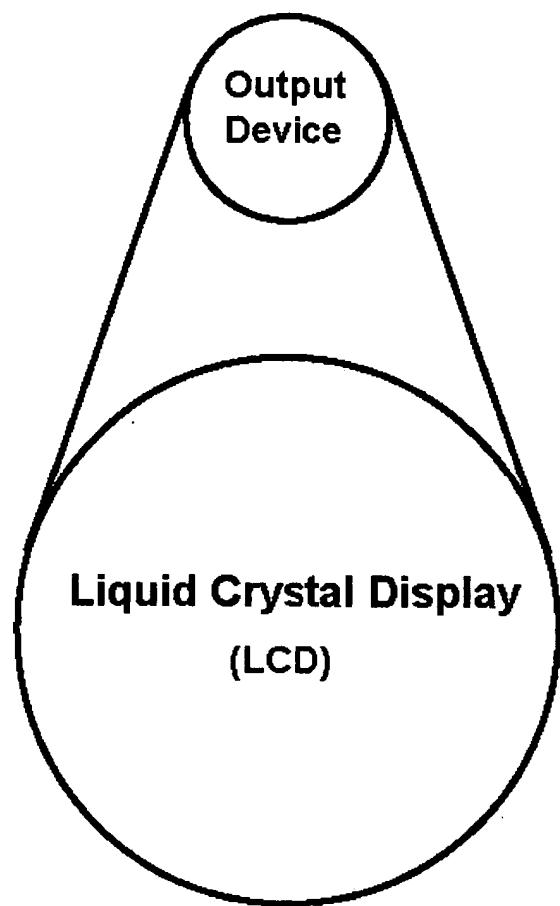


FIG. 6 The Output Display-LCD

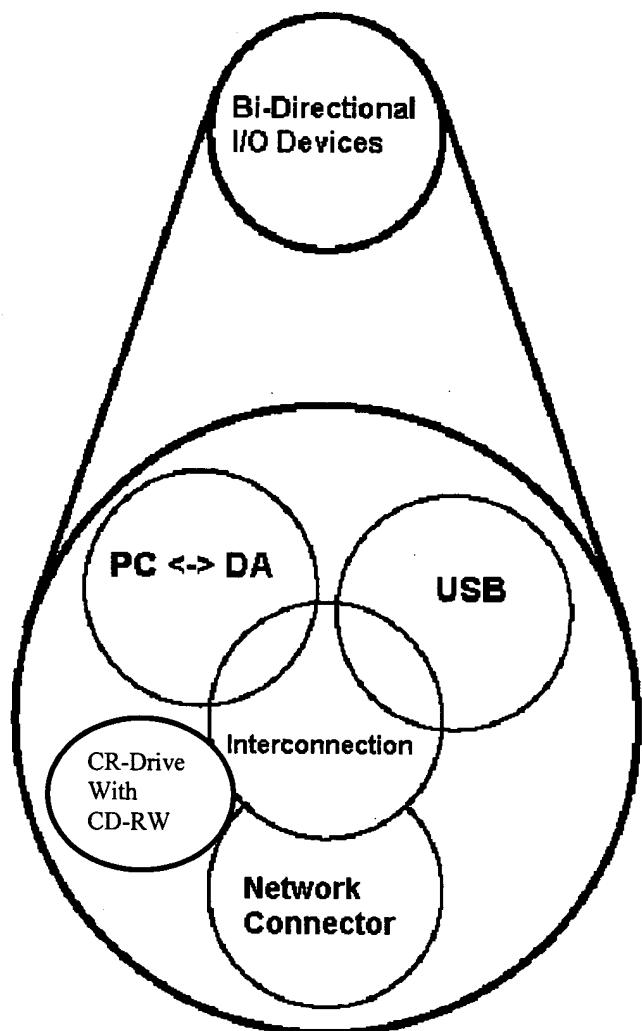


FIG. 7 The Bi-directional I/O

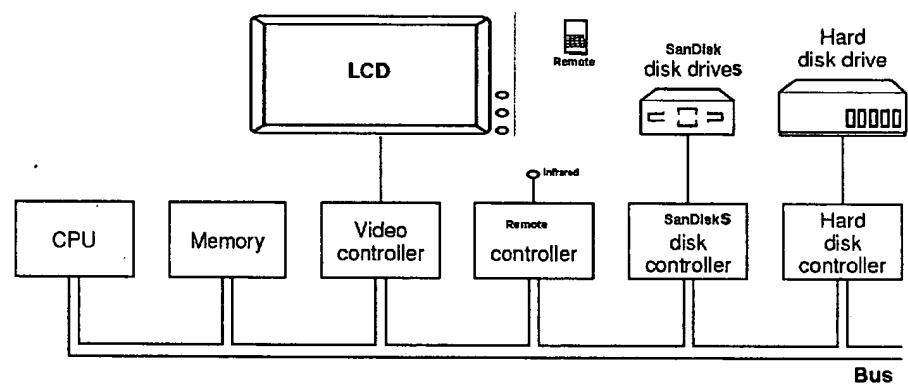


FIG. 8 The System Bus: Type 1

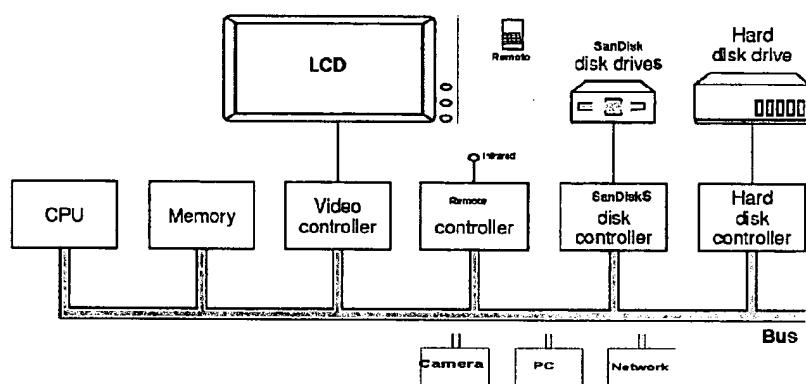


FIG. 9 The System Bus: Type 2

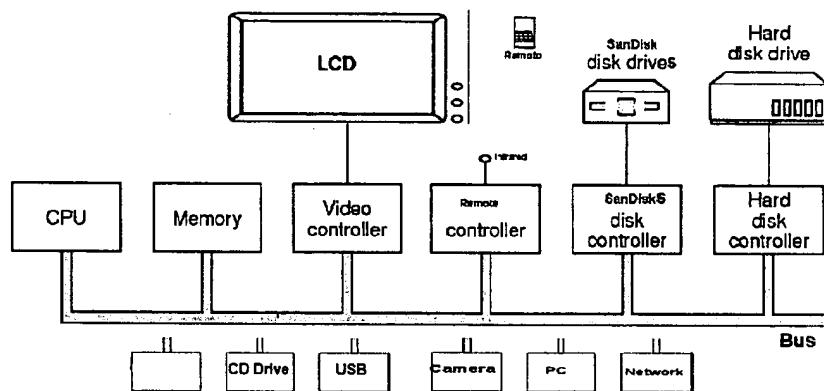


FIG. 10 The System Bus: Type 3

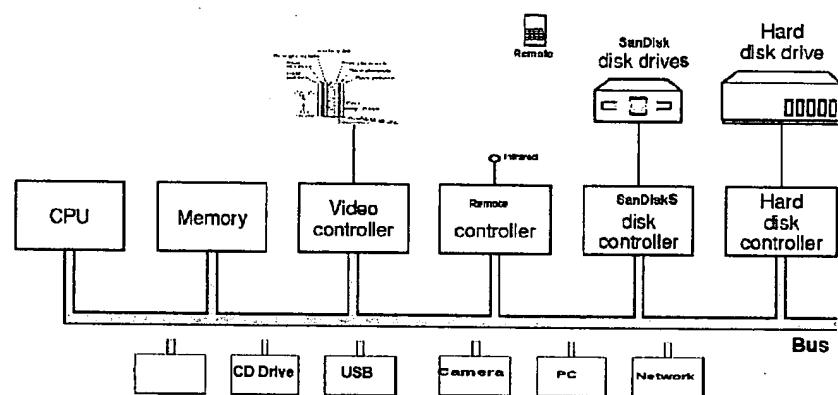


FIG. 11      System Bus: Type 4

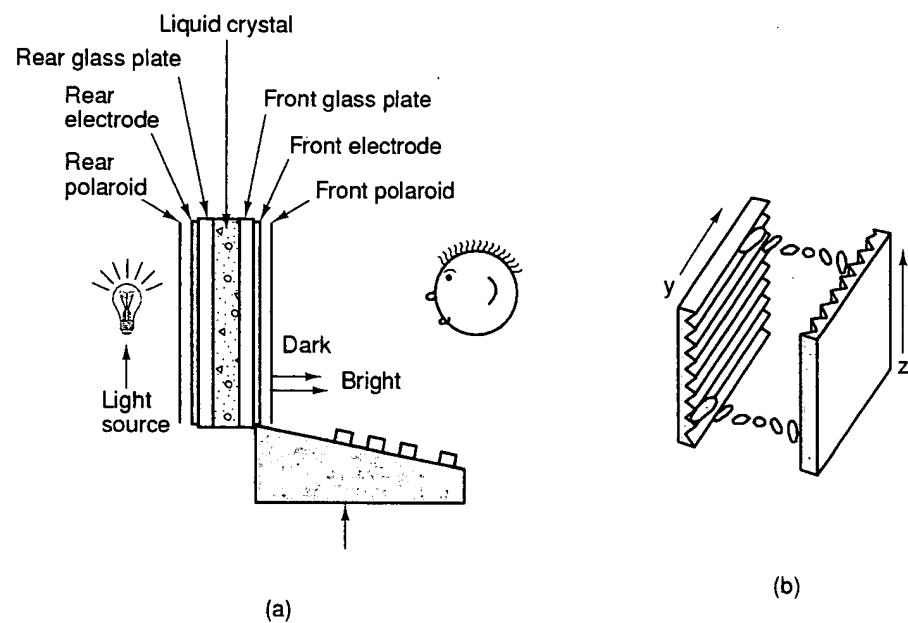


FIG. 12 Top-Level Structure of DigiNote

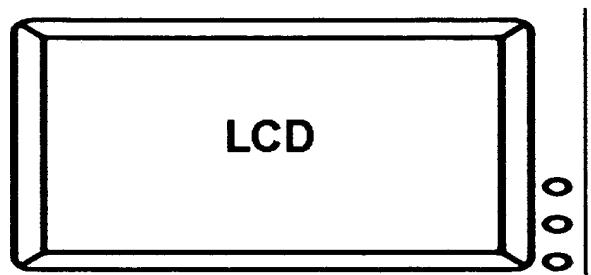


FIG. 13 LCD Top-Level Structure

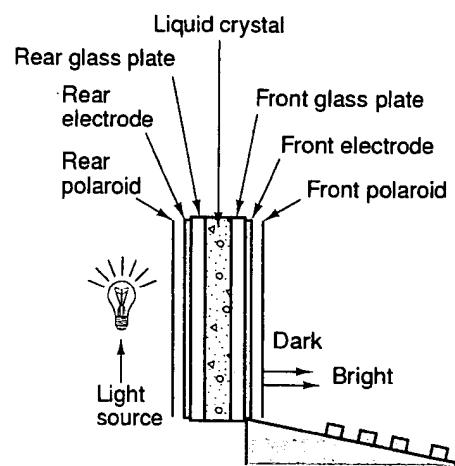


FIG. 14: The DigiNote

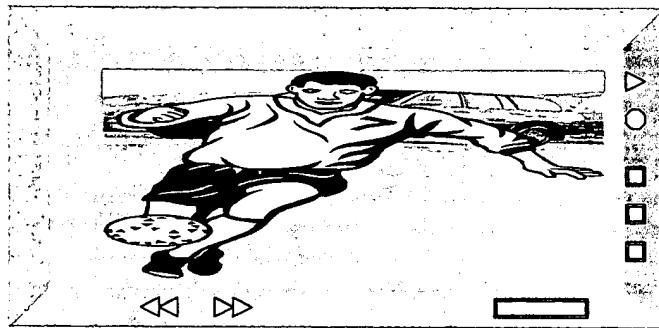


FIG 16A      The DigiMount



FIG. 16B      The DigiMount

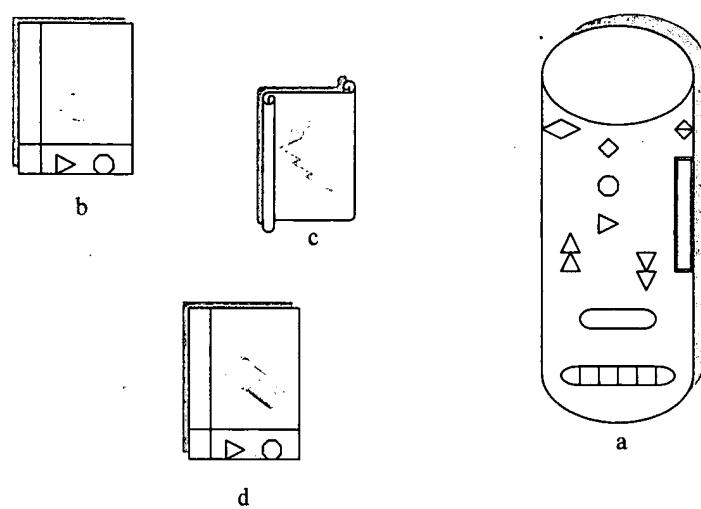


FIG. 16C The DigiServer (a) and its PDA or Terminals (b, c, d)

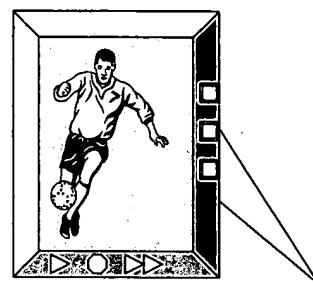


FIG. 16D

The DigiMin

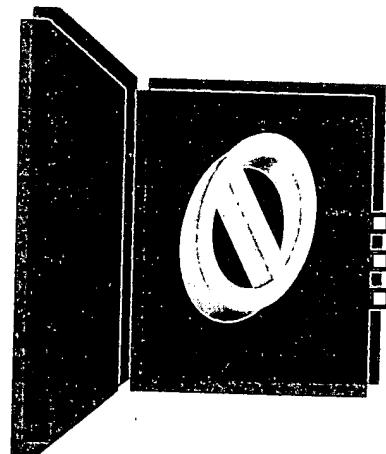


FIG 16E The DigiNote

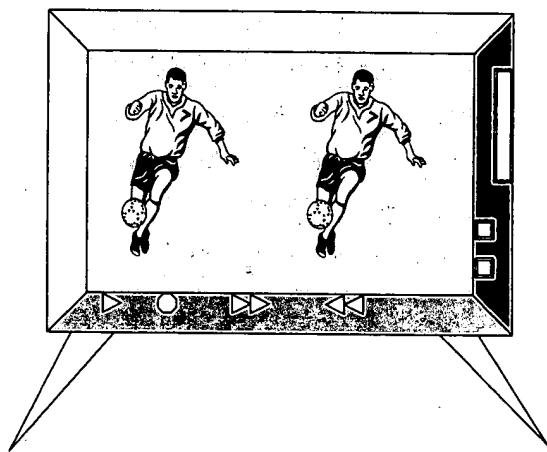


FIG. 16F      The DigiDesktop